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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant:	Jeffrey R. Wilcox, et al.	§	Group Art Unit:	2187
Serial No.:	10/038,960	§	Examiner:	Christian Chase
Filed:	January 2, 2002	§	Assignee:	Intel Corporation
For:	Method and Apparatus For Reducing Power Consumption In A Memory Bus Interface By Selectively Disabling And Enabling Sense Amplifiers	§	Atty. Dkt. No.:	ITL.0668US (P12986)

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION MAILED ON JUNE 22, 2005

Dear Sir:

Please consider the comments in the following REMARKS section.

Date of Deposit:	August 22, 2005
I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-8340) on the date indicated above.	
Janice Munoz	

*Janice Munoz*